

VLSI DESIGN AND VERILOG HDL

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PREFACE

This book provides a comprehensive study and concept clearing for B. Tech. /M. Tech. students. It is our intent to give the students the best available latest information and concepts on the subject. The organization of the book is organized in such a way that it makes strong foundation and depth knowledge of VLSI DESIGN AND VERILOG HDL.

Since high technology changes rapidly, we have presented the material in a generic manner, unbiased toward particular architecture implementations. To pace with technology changes, frequent updates with newer editions become a necessity, and we plan to make revisions every year in future.

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