COMPUTER ORGANIZATION AND ARCHITECTURE

P.Dhivya (*Assistant Professor – CSE Department*)

C.B.Sivaparthipan (*Assistant Professor – CSE Department*)

Dr.R.M. Bhavadharini (*Associate Professor – CSE Department*)

Dr.T. Kalaikumaran (*HOD – CSE Department*)

Dr. N. Karthikeyan (*HOD – Comp.App. Department*)

SNS COLLEGE OF TECHNOLOGY
COIMBATORE, TAMILNADU, INDIA.

COMPUTER ORGANIZATION AND ARCHITECTURE

P R E F A C E

It is with great pleasure that we are bringing out the first edition of this book. The book happens to be covering the latest trends in Computer Organization and Architecture, We hope that the present edition lives up to the challenge, and provides a power-packed which has been developed from scratch. We are confident that the reader would find immense value in this book for keeping pace with the changing paradigm of Computer organization and Architecture, and that sufficient concept as well as depth is provided to give the reader a feeling of complete understanding of the whole thing.

Here is a summary of the content and structure of the book:

- Fundamentals units of computer
- Basic operational concepts
- Memory location and addresses
- Addressing modes
- Random Access Memory (RAM)
- Internal Organization of Semiconductor Memory Chips
- Cache and Virtual Memory
- Secondary storage
- Accessing I/O devices
- Interrupts
- Direct Memory Access
- Buses and Interface Circuits.

A C K N O W L E D G E M E N T

C O N T E N T S

CHAPTER 2
MEMORY SYSTEM.. 31

CHAPTER 3
INPUT/OUTPUT ORGANIZATION 55